Solid-state Counter

## Choice of Functional Modules for an Arbitrary Counter System Configuration

Compact (same size as thumbwheel switch modules)
■ Capability to both read in and read out BCD data

- Building-block type modules permit easy assembly of a counter system with up to eight digits plus inputs and outputs
- Simple multi-stage preset/totalizing counter configuration up to eight stages
■ Number system selectable (6, 24, 60, any other base may be used)
- Initial set mode to allow counting from an arbitrary value



## Ordering Information

| Model | Function |
| :---: | :---: |
| Counter Unit H7SA | - Serves as a single-digit element of a multi-digit Counter. <br> - Reads in and out BCD data. <br> - Displays a digit by backlighted LCD. <br> - May be combined with up to seven other Counter elements to form an 8 -digit Counter (maximum). <br> - Specifies a selectable number system. |
| Input-Output Unit H7SB-A | - Specifies the input-output mode of the Counter. <br> - Specifies the counting speed of the Counter. <br> - Selects either Initial Set or Preset mode. <br> - Communicates with up to eight H7SA Counters. <br> - Generates output when the count value of the Counter coincides with the preset value. <br> - Provides open collector output for loads. |
| Preset Output Unit H7SB-B | - Generates coincidence output in multistage Counter system configuration. <br> - Communicates with up to eight decimal-coded thumbwheel switch modules. <br> - Provides open collector output for loads. |
| Fan-out Unit H7SB-C | - Amplifies signals from the H7SA Counters. <br> - Converts BCD signals into decimal or BCD/BCD (Signal Distributor) (real/complementary code) data. <br> - Distributes signals to respective stages in multistage Counter system configuration. |
| Battery Unit H7SB-D | - Serves for battery backup to retain the contents of memory (i.e. count data) in the event of a power failure. <br> - The life expectancy of the battery is five years at $20^{\circ} \mathrm{C}$ with the 8 -digit model of model H7SA. |

## Accessories (Order Separately)

| Card-edge Connector | P7S-22P |
| :--- | :--- |
| End Caps (pair) | A7P-M |

## ■ Combination Examples

Read-out Counter (A)


Read-out Counter (B)


## Read-in/Read-out Counter



Counter with Selectable Number System


Preset Counter


## Specifications

## - Ratings

| Supply voltage | 5 VDC $\pm 10 \%$ (contains $10 \%$ ripple) |
| :--- | :--- |
| Power consumption | H7SA: 75 mW per Unit <br> H7SB-A: 75 mW per Unit <br> H7SB-B: 75 mW per Unit <br> H7SB-C: 50 mW per Unit |
| Max. counting speed | $30 \mathrm{cps} / 1,000 \mathrm{cps}$ (wire-selectable) |
| Count input | No voltage input <br> Short-circuit impedance: $1 \mathrm{k} \Omega$ max. <br> Short-circuit residual voltage: 1.0 V max. <br> (current flow through count input terminal when shorted: 0.5 mA ) <br> Open-circuit impedance: $100 \mathrm{k} \Omega$ min. <br> Voltage input: <br> HIGH level: 4 to 30 V <br> LOW level: 0 to 2 V (Input resistance: $27 \mathrm{k} \Omega$ ) |
| Reset system | External reset (reset signal time: 0.02 s ) |
| Control output | Open collector: 30 VDC max., 100 mA max., ON residual voltage: 0.5 V max. |
| BCD input (to H7SA) | Short-circuit impedance: $1 \mathrm{k} \Omega$ max. <br> Short-circuit residual voltage: 1.0 V max. (breakdown voltage: 5 V ) <br> Open-circuit impedance: $100 \mathrm{k} \Omega$ min. |
| BCD output (from H7SA) | Open drain: 5 V 1 mA (breakdown voltage: 5 V ) |
| Decimal or BCD/BCD output (from |  |
| H7SB-C) (see note) | Open collector: $30 \mathrm{VDC} 20 \mathrm{~mA}, \mathrm{ON}$ residual voltage: 0.5 V max. |

Note: BCD: BCD complementary code

## ■ Characteristics

| Insulation resistance | $100 \mathrm{M} \Omega \mathrm{min}$. (at 500 VDC ) |
| :--- | :--- |
| Dielectric strength | $1,000 \mathrm{VAC} 50 / 60 \mathrm{~Hz}$ for 1 minute |
| Vibration resistance | Destruction: $\quad 10$ to $55 \mathrm{~Hz} ; 0.75 \mathrm{~mm}$ double amplitude <br> Malfunction: $\quad 10$ to $55 \mathrm{~Hz} ; 0.3 \mathrm{~mm}$ double amplitude |
| Shock resistance | Destruction: $\quad 300 \mathrm{~m} / \mathrm{s}^{2}$ (approx. 30 G ) <br> Malfunction: $\quad 100 \mathrm{~m} / \mathrm{s}^{2}$ (approx. 10 G ) |
| Ambient temperature | Operating: $-10^{\circ} \mathrm{C}$ to $55^{\circ} \mathrm{C}$ (with no icing) <br> Storage: $-25^{\circ} \mathrm{C}$ to $65^{\circ} \mathrm{C} \mathrm{(with} \mathrm{no} \mathrm{icing)}$ <br> Ambient humidity |

## Dimensions



Panel Cutouts for Flush Mounting
When Multiple H7SAs (n Digits) are Combined with One H7SB-A


When Multiple A7PSs (n Digits) are Combined with One H7SB-B in Multistage Counter Configuration


When Multiple H7SAs (n Digits) are Combined with One H7SB-A and One H7SB-D


When Multiple H7SB-Cs (n Digits) are Used

$A=(10 \times n)+12$
$B=(10 \times n)+9$


Connector Mounting Holes for Front Mounting
When Multiple H7SAs (n Digits) are Combined with One H7SB-A


When Multiple H7SAs (n Digits) are Combined with One H7SB-A and One H7SB-D


When Multiple H7SB-Cs (n Digits) are Used


## Timing Charts/Operation Modes

■ Operations (Basic Combination of H7SA with H7SB-A) Preset Mode


Initial Set Mode


## Input Mode

Note: 1. No-voltage input type
$\mathrm{H}: 1 \mathrm{k} \Omega$ max. of impedance when the contact turns ON
1 V max. of residual voltage when the contact turns ON
$\mathrm{L}: 100 \mathrm{k} \Omega \mathrm{min}$. of impedance when the contact turns OFF
Voltage input type
H : Input signal voltage +4 to +30 V
L: Input signal voltage 0 to 2 V
2. Up/Down A Counter (Command input)

The duration of A must be more than the minimum signal width ( $30 \mathrm{cps}: 16.7 \mathrm{~ms} ; 1 \mathrm{kcps}: 0.5 \mathrm{~ms}$ ).
3. Up/Down B Counter (Separate input)

There is no limitation on the phase relationship between Count input 1 and Count input 2.
4. Up/Down C Counter (Phase differential input)

The duration of $B$ must be more than half the minimum signal width. Count input 1 and Count input 2 must be of the same counting speed.


Basic Operation Mode (H7SA + H7SB-A)
(Basic Combination of H7S with H7SB-A)

Control output \begin{tabular}{l}

| Self-holding |
| :--- |
| output |


 

One-shot output <br>
(fixed to 0.5 s )
\end{tabular}

Preset Mode (Up/Down A, B, C, and Up Type)

|  | Timing chart | Configuration |
| :---: | :---: | :---: |
| N |  | Note: As soon as the preset value is changed |
| K |  | 9 9 9 <br> 9 9 Thumbwheel switches$\quad$(for $B C D$ |

Initial Set Mode (Up/Down A, B, C)

|  | Timing chart | Configuration |
| :---: | :---: | :---: |
| N |  |  |
| K |  |  |

## Applied Operation Mode (H7SA + H7SB-A + H7SB-C + H7SB-B)

(In the case of multiple stages and digits)
Set the H7SA in K mode.
Preset Mode (Up/Down A, B, C)
(See note 1.)

| Timing chart | Configuration |
| :---: | :---: |
|  | Note: H7SB-C: decimal code mode |

Note: 1. The above timing chart applies to the H7SB-A in preset without presetting thumbwheel switches connected.
2. "Preset" means a value preset by the thumbwheel switches combined with the H7SB-B. Control outputs 1 and 2 respectively indicate the outputs to be produced by the H7SB-B when present value coincides with presets 1 and 2 .
Initial Set Mode (Up/Down A, B, C)


Note: 1. The H7SB-A produces its output when the present count value is 0 in initial set mode.
2. "Preset" means a value preset by the thumbwheel switches combined with the H7SB-B. Control outputs 1 and 2 respectively indicate the outputs to be produced by the H7SB-B when present value coincides with presets 1 and 2.

## Functions of I/O Terminals

## ■ H7SA

Block Diagram


I/O Signals

| Terminal No. | Signal | Function |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: |
| A1 | COM | 0 V terminal |  |  |  |
| A2 | DA | Terminals for BCD data input from external devices such as thumbwheel switch and PC. Each terminal represents the following place value: |  |  |  |
| A3 | DB |  |  |  |  |
| A4 | DC | $\frac{\mathrm{DA}}{1}$ | $\frac{\mathrm{DB}}{2}$ | $\frac{\mathrm{DC}}{4}$ | $\frac{\mathrm{DD}}{8}$ |
| A5 | DD |  |  |  |  |
| A6 | Vacant | --- |  |  |  |
| A7 | DP | Terminal for specifying the decimal point lighting on LCD. The decimal point illuminates when this terminal is connected to any of the COM terminals. |  |  |  |
| A8 | XM | Terminal for setting the arbitrary number system function. When this terminal is connected to any of the COM terminals, any number system may be specified. |  |  |  |
| A9 | Vacant | --- |  |  |  |
| A10 | Cl | Terminal for count pulse input from the low-order digit. (The 1st digit is for pulse input from the H7SB-A.) |  |  |  |
| A11 | Vacant | --- |  |  |  |
| B1 | X | Terminal for setting any base when the arbitrary number system function is in effect. This terminal is connected to terminals $\mathrm{B} 2(\mathrm{OA})$ through B 5 (OD). |  |  |  |
| B2 | OA | Terminals for BCD data output to external devices such as PC and display. Each terminal represents the following place value. |  |  |  |
| B3 | OB |  |  |  |  |  |  |
| B4 | OC | $\frac{O A}{1}$ | $\frac{\mathrm{OB}}{2}$ |  | $\frac{\mathrm{OD}}{8}$ |
| B5 | OD |  |  |  |  |
| B6 | Vacant | --- |  |  |  |
| B7 | COM | 0 V terminal |  |  |  |
| B8 | COM | 0 V terminal |  |  |  |
| B9 | Vacant | --- |  |  |  |
| B10 | CO | Terminal for count pulse output (carry) to the high-order digit. |  |  |  |
| B11 | Vacant | --- |  |  |  |

Output Status of OA to OD

| Count value | OA | OB | OC | OD |
| :--- | :--- | :--- | :--- | :--- |
| 0 | 0 | 0 | 0 | 0 |
| 1 | 1 | 0 | 0 | 0 |
| 2 | 0 | 1 | 0 | 0 |
| 3 | 1 | 1 | 0 | 0 |
| 4 | 0 | 0 | 1 | 0 |
| 5 | 1 | 0 | 1 | 0 |
| 6 | 0 | 1 | 1 | 0 |
| 7 | 1 | 1 | 1 | 0 |
| 8 | 0 | 0 | 0 | 1 |
| 9 | 1 | 0 | 0 | 1 |
| ON: 0, OFF: 1 |  |  |  |  |

## ■ H7SB-A

## Block Diagram



I/O Signals

| Terminal No. | Signal | Function |
| :--- | :--- | :--- |
| A1 | COM | 0 V terminal |
| A2 | RE (N) | Reset input terminal <br> (No voltage input: Reset input signal is applied when this terminal is short-circuited or <br> opened.) |
| A3 | CP2 (N) | Count input terminals <br> (No voltage input: CP2 and CP1 are applied when these terminals are short-circuited or <br> opened.) |
| A4 | CP1 (N) | --- |
| A5 | Vacant | Terminal for specifying the operation mode of control output. The operation mode is set to "N" <br> mode when this terminal is connected to any of the COM terminals and to "K" mode when left <br> open. |
| A6 | Terminal for specifying the maximum counting speed of the Counter. The counting speed is <br> set to 30 cps when this terminal is connected to any of the COM terminals and to 1,000 cps <br> when left open. |  |
| A7 | FM |  |

\begin{tabular}{|c|c|c|}
\hline Terminal No. \& Signal \& Function <br>
\hline A8

A9 \& IM2 \& | Terminals for input mode specification. |
| :--- |
| Note: "L" refers to IM1 or IM2 connected to any of the COM terminals, while "H" refers to IM1 or IM2 left open. | <br>

\hline A10 \& DM \& Terminal for data mode specification. The data mode is set to "Initial Set" mode when this terminal is connected to terminal COM and to "Preset" mode when left open. <br>
\hline A11 \& +B \& Terminal for control power supply application <br>
\hline B1 \& OUT \& Terminal for control output when the count value of H7SA coincides with its preset value with respect to all digits. <br>
\hline B2 \& RE (V) \& Reset input terminal (Voltage input) <br>
\hline B3 \& CP2 (V) \& Count input terminals <br>
\hline B4 \& CP1 (V) \& (Voltage input) <br>
\hline B5 \& Vacant \& --- <br>
\hline B6 \& COM \& 0 V terminal <br>
\hline B7 \& COM \& 0 V terminal <br>
\hline B8 \& COM \& 0 V terminal <br>
\hline B9 \& COM \& 0 V terminal <br>
\hline B10 \& CO \& Terminal for count pulse output to H7SA <br>
\hline B11 \& +B' \& Power terminal for battery backup <br>
\hline
\end{tabular}

## H7SB-B

## Block Diagram



I/O Signals

| Terminal No. | Signal |  |
| :--- | :--- | :--- |
| A1 | COM | O V terminal |
| A11 | +B | Terminal for control power supply application |
| B1 | OUT | Terminal for control output when the input value of H7SB-C coincides with its preset value <br> with respect to all digits |
| B2 | IN1 | Terminals for coincidence signal input from thumbwheel switches (decimal-coded). |
| B3 | IN2 |  |
| B4 | IN3 |  |
| B5 | IN4 |  |
| B6 | IN5 |  |
| B7 | IN6 |  |
| B8 | IN7 |  |
| B9 | IN8 |  |

## - H7SB-C

## Block Diagram



Low level signal is input to H7SB-C.
Short-circuit impedance: $1 \mathrm{k} \Omega$
Open-circuit impedance: $100 \mathrm{k} \Omega$
Short-circuit residual voltage: 1.0 V
I/O Signals

| Terminal No. | Signal | Function |  |  |
| :---: | :---: | :---: | :---: | :---: |
| A1 | COM | 0 V terminal |  |  |
| $\begin{aligned} & \text { A2 } \\ & \text { A3 } \\ & \text { A4 } \\ & \text { A5 } \end{aligned}$ | $\begin{aligned} & \hline \mathrm{DA} \\ & \mathrm{DB} \\ & \mathrm{DC} \\ & \mathrm{DD} \\ & \hline \end{aligned}$ | Terminals for BDC data input from H7SA. Each terminal represents the following place value: DA/1 DB/2 DC/4 DD/8 |  |  |
| A11 | +B | Terminal for control power supply application |  |  |
| $\begin{aligned} & \hline \text { B1 } \\ & \text { B2 } \\ & \text { B3 } \\ & \text { B4 } \\ & \text { B5 } \\ & \text { B6 } \\ & \text { B7 } \\ & \text { B8 } \\ & \text { B9 } \\ & \text { B10 } \end{aligned}$ | $\begin{aligned} & \hline \text { OUT0 } \\ & \text { OUT1 } \\ & \text { OUT2 } \\ & \text { OUT3 } \\ & \text { OUT4 } \\ & \text { OUT5 } \\ & \text { OUT6 } \\ & \text { OUT7 } \\ & \text { OUT8 } \\ & \text { OUT9 } \end{aligned}$ | Terminals for decimal or BCD data output as specified by B/D (No. B11).Signal Output |  |  |
|  |  |  |  |  |
|  |  |  | BCD code | Decimal code |
|  |  | OUTO | $\bar{A}$ | 0 |
|  |  | OUT1 | $\overline{\text { B }}$ | 1 |
|  |  | OUT2 | $\overline{\text { C }}$ | 2 |
|  |  | OUT3 | $\overline{\text { D }}$ | 3 |
|  |  | OUT4 | --- | 4 |
|  |  | OUT5 | A | 5 |
|  |  | OUT6 | B | 6 |
|  |  | OUT7 | C | 7 |
|  |  | OUT8 | D | 8 |
|  |  | OUT9 | --- | 9 |
|  |  | In the above table, A, B, C, and D indicate place values $1,2,4$, and 8 , respectively. Do not use OUT 4 and 9 when using BCD codes. |  |  |
| B11 | B/D | Terminal for specifying the contents of data on output terminals. When this terminal is connected to COM, decimal codes are output from OUT0 through OUT9. When left open, BCD codes are output from OUT0 through OUT3 and OUT5 through OUT8. |  |  |

Output Status of OUT 0 to 9

| Input |  |  |  | Decimal code |  |  |  |  |  |  |  |  |  | BCD code |  |  |  |  |  |  |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  |  |  |  | OUT |  |  |  |  |  |  |  |  |  | OUT |  |  |  |  |  |  |  |  |  |
| DA | DB | DC | DD | 0 | 1 | 2 | 3 | 4 | 5 | 6 | 7 | 8 | 9 | 0 | 1 | 2 | 3 | 4 | 5 | 6 | 7 | 8 | 9 |
| 0 | 0 | 0 | 0 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 1 | 1 | 1 | 1 | 0 | 0 | 0 | 0 | 0 | 0 |
| 1 | 0 | 0 | 0 | 0 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 1 | 1 | 1 | 0 | 1 | 0 | 0 | 0 | 0 |
| 0 | 1 | 0 | 0 | 0 | 0 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 1 | 0 | 1 | 1 | 0 | 0 | 1 | 0 | 0 | 0 |
| 1 | 1 | 0 | 0 | 0 | 0 | 0 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 1 | 1 | 0 | 1 | 1 | 0 | 0 | 0 |
| 0 | 0 | 1 | 0 | 0 | 0 | 0 | 0 | 1 | 0 | 0 | 0 | 0 | 0 | 1 | 1 | 0 | 1 | 1 | 0 | 0 | 1 | 0 | 0 |
| 1 | 0 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 1 | 0 | 0 | 0 | 0 | 0 | 1 | 0 | 1 | 0 | 1 | 0 | 1 | 0 | 0 |
| 0 | 1 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 1 | 0 | 0 | 0 | 1 | 0 | 0 | 1 | 0 | 0 | 1 | 1 | 0 | 0 |
| 1 | 1 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 1 | 0 | 0 | 0 | 0 | 0 | 1 | 0 | 1 | 1 | 1 | 0 | 0 |
| 0 | 0 | 0 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 1 | 0 | 1 | 1 | 1 | 0 | 0 | 0 | 0 | 0 | 1 | 0 |
| 1 | 0 | 0 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 1 | 0 | 1 | 1 | 0 | 0 | 1 | 0 | 0 | 1 | 1 |
| ON: 0, OFF: 1 |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |

## - H7SB-D

Block Diagram


Terminal Arrangement when Connector is Mounted

(REAR VIEW)

## Accessories (Order Separately)

P7S-22P Card-edge Connector


## Mounting Height with Connector

## Flush Mounting



## Front Mounting



## A7P-M End Caps

End Caps are attached to each end of the assembled Counter system components and are used to secure the Counter system to a mounting panel.


## Precautions

Flush Mounting
After assembling the component units with end caps at both ends, push the assembly into the panel cutout and make sure that it is secured and flush with the surface of the mounting panel.


## Connections

Data Input Terminals
The data input terminal circuits of both the H7SA Counter Unit and H7SB-C Fan-out Unit are as shown below.

(No Voltage Input)
For no voltage input, either of the following methods of connection is recommended for the data input terminals (DA through DD) of both the H7SA and H7SB-C Units.

(Voltage Input)
If it is likely that a voltage of 5 V or more will be applied to the data input terminals of the H7SA and H7SB-C Units, be sure to insert a diode as shown in the diagram below.


Note: When the external device is in the ON state, Vi must be 1 V or less.

## Card-edge Connector

Each component unit is externally connected through a Card-edge Connector. Leave intact the edge of the PC board except when mounting the Card-edge Connector (P7S-22P) supplied as an exclusive but optional accessory.
Do not solder any leads to the I/O terminals on the board edge.


ALL DIMENSIONS SHOWN ARE IN MILLIMETERS.
To convert millimeters into inches, multiply by 0.03937 . To convert grams into ounces, multiply by 0.03527 .

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